



AL16937
BUCK DIMMABLE LED DRIVER

Description

The AL16937 is a high performance, high power factor, high efficiency, and high current precision buck dimmable LED driver for triac dimmable LED lamp applications. The AL16937 topology provides an accurate output current over wide line and load regulation. The wide switching frequency operates at boundary conduction mode (BCM) to ease EMI/EMC design and testing, to meet the latest regulatory standards.

The AL16937 LED driver integrates 400V/3A MOSFET, which is suitable for $120V_{AC}$ applications. The AL16937 has the built-in thermal fold-back protection trigger point to automatically reduce output current. Other protection features enhance LED lighting system's safety and reliability.

The AL16937 dimming curve is compliant with the NEMA SSL6 standard. The AL16937 applies to a wide range of dimmers, including leading edge and trailing edge dimmer, to achieve deep dimming down to 1%.

The AL16937 is available in SO-7 package.

Features

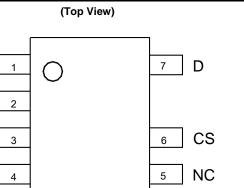
- Tight Current Sense Tolerance: ± 3%
- Low Startup Current: 100μA Typical
- Low Operation Current: 210µA (Switching Frequency at 4kHz)
- Single Winding Inductor
- Wide Range of Dimmer Compatibility
- Integration of 400V/3A MOSFET
- NEMA SSL6 Dimming Curve Compliant
- Internal Protections
 - Under Voltage Lockout (UVLO)
 - Leading-Edge Blanking (LEB)
 - Cycle-By-Cycle Over Current Protection (OCP)
 - Output Short Protection (OSP)
 - Thermal Foldback Protection (TFP)
 - Over-Temperature Protection (OTP)
- SO-7 Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments

VCC

COMP

GND



SO-7

Applications

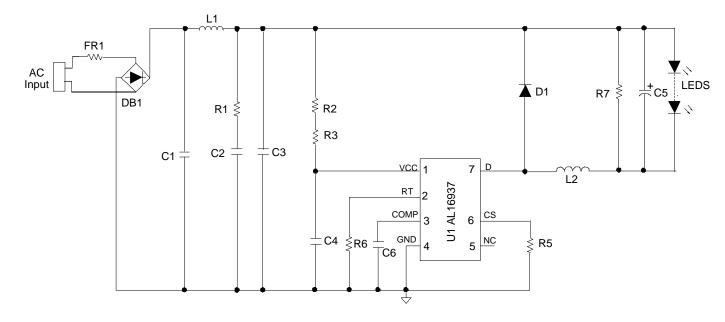
- Mains Dimmable LED Lamps
- Offline LED Power Supply Driver

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Typical Application Circuits



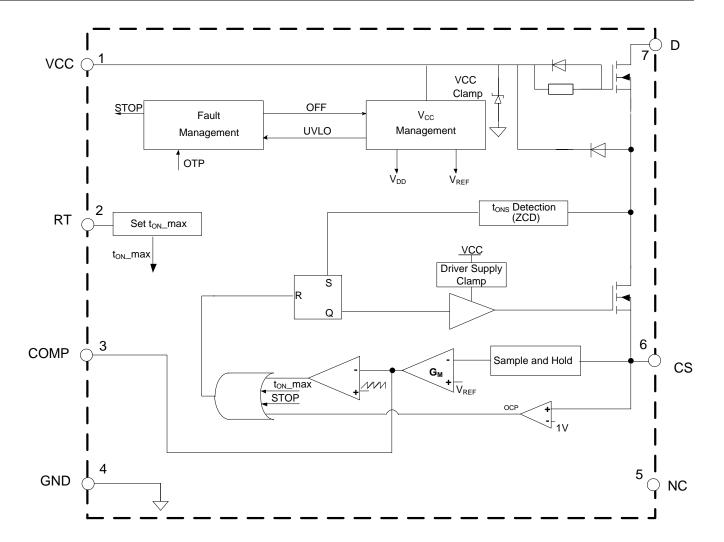
AL16937 Buck Application Circuit

Pin Descriptions

Pin Number	Pin Name	Function			
1	VCC	Power supply voltage			
2	RT	Resistor set the system's maximum t _{ON}			
3	COMP	Compensation for current control			
4	GND	Ground			
5	NC	Not connected			
6	CS	Current sensing			
7	D	Drain of the internal high voltage MOSFET			



Functional Block Diagram



AL16937



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.) (Note 4)

Symbol	Parameter	Rating	Unit
V _{CC}	Power Supply Voltage	18	V
V_D	Voltage on Drain Pin	400	V
I _{DS}	Continuous Drain Current T _C = +25°C	3	Α
V _{CS}	Voltage on CS Pin	-0.3 to 7	V
V_{RT}	Voltage on RT Pin	-0.3 to 7	V
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
T _{LEAD}	Lead Temperature (Soldering, 10s)	+260	°C
P _D	Power Dissipation (T _A = +50°C) (Note 5)	0.8	W
θ_{JA}	Thermal Resistance (Junction to Ambient) (Note 5)	123	°C/W
θЈС	Thermal Resistance (Junction to Case) (Note 5)	19	°C/W
	ESD (Human Body Model)	2,000	V
_	ESD (Machine Model)	200	V

Notes:

- 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability. All voltages unless otherwise stated and measured with respect to GND.
- 5. Device mounted on 1"x1" FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.

Recommended Operating Conditions (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
T _A	Ambient Temperature (Note 6)	-40	+105	°C

Note: 6. The device may operate normally at +125°C ambient temperature under the condition not trigger temperature protection.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Condition	Min	Тур	Max	Unit
UVLO						
V _{TH (ST)}	Startup Voltage	Startup Voltage –		14.5	_	V
V _{OPR(Min)}	Minimal Operating Voltage	After Turn On	_	8.5	_	V
V _{CC_CLAMP}	V _{CC} Clamp Voltage	I _{CC} = 1mA	-	15.5	_	V
Standby Current						
I _{ST}	Start-Up Current	V _{CC} = V _{TH (ST)} -0.5V, Before Start Up	-	100	_	μA
I _{CC (OPR)}	Operating Current	Switching Frequency at 4kHz	-	210	_	μA
Source Driver						
R _{DS(ON)_} LV	Internal Low Voltage MOSFET On- State Resistance (Note 7)	_	_	1	-	Ω
High Voltage and Super	r-Junction MOSFET					
R _{DS(ON)_HV}	Drain-Source On-State Resistance	AL16937-30BA	-	2.8	3.4	Ω
V_{DS}	Drain-Source Breakdown Voltage	AL16937-30BA	400	_	_	V
I _{DSS}	Drain-Source Leakage Current	AL16937-30BA	-	_	1	μΑ
RT						
V_{RT_REF}	Reference Voltage of RT pin	_	-	0.5	_	V
Current Sense						
V _{CS_CLAMP}	CS Clamp Voltage	_	-	1	-	V
V_{REF}	Internal Current Loop Control Reference	_	0.388	0.4	0.412	V
ton_min	Minimum t _{ON}	_	-	550	_	ns
t _{ON_MAX}	Maximum t _{ON}	$R_T = 51k\Omega, V_{COMP} = 4V$	ı	4.7	-	μs
t _{OFF_MIN}	Minimum t _{OFF} (Note 7)	_	-	4	_	μs
t _{OFF_MAX}	Maximum t _{OFF}	_	-	290	_	μs
Error Amplifier						
G_M	Gm Trans-Conductance	_	-	25	-	μA/V
I _{SOURCE}	Amplifier Source Current	V _{CS} = 0V	-	10	_	μΑ
I _{SINK}	Amplifier Sink Current	V _{CS} =1.5V	-	9	_	μΑ
Thermal Foldback and	Over Temperature Protection (OTP)					
T _{FOLD}	Thermal Foldback (Note 7)	-	_	+145	_	°C
_	Thermal Shutdown (Notes7&8)	_	-	+160	_	°C

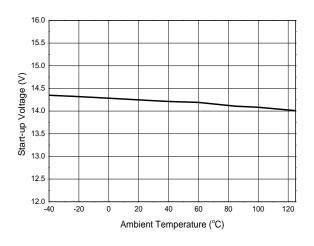
Notes:

^{7.} These parameters, although guaranteed by design, are not tested in production.8. The device will latch off when OTP happens, recovered after power cycle and the device won't operate normally at this temperature.

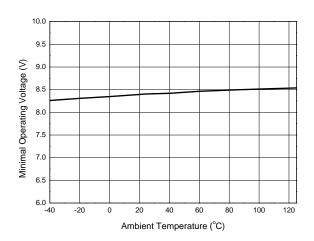


Performance Characteristics (Note 9)

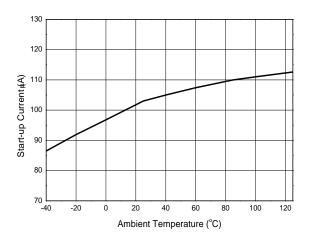
Start-up Voltage vs. Ambient Temperature



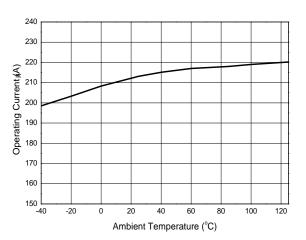
Minimum Operating Voltage vs. Ambient Temperature



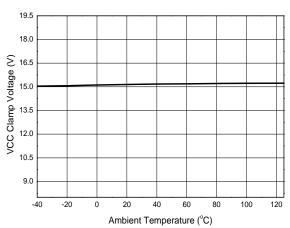
Start-up Current vs. Ambient Temperature



Operating Current vs. Ambient Temperature



VCC Clamp Voltage vs. Ambient Temperature



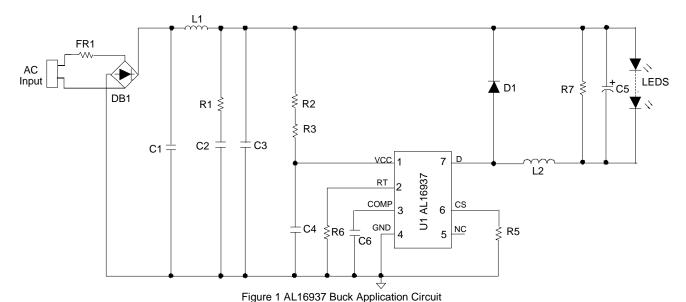
Note: 9. These electrical characteristics are tested under DC condition. The ambient temperature is equal to the junction temperature of the device.



Operation

The AL16937 is a single stage, single winding, high efficiency, and high power factor dimmable LED driver for triac dimmable LED lamp applications. The AL16937 LED driver integration of 400V/3A MOSFET can cover 120Vac application.

The AL16937 adopts source-driver technique to decrease the system operating current. It uses a novel method to detect the tope time which results in the removal for the need of an auxiliary winding. The AL16937 operates at boundary conduction mode (BCM) which can ease EMI design and achieve high efficiency. High power factor (HPF) is achieved by using constant on-time mode; coupled with a closed loop of constant current control, the AL16937 achieves good line and load regulation



Start-Up and Supply Voltage

Before start-up, the V_{CC} capacitor C4 is charged by the startup resistors (R2, R3) from the high voltage mains. When the start-up voltage is reached, the AL16937 starts switching. During normal operation, the V_{CC} supply is provided by start-up resisters (R2, R3) and the output voltage (V_{OUT}) rectified by one diode (D2). In this way the system can provide V_{CC} supply at low dimming angle.

The AL16937 has an internal VCC clamp voltage (typical 15.5V), which is limited by one internal active Zener diode.

When VCC voltage drops to below the $V_{OPR(MIN)}$, switching is stop. So the device can operate normally when the voltage on VCC pin is between $V_{OPR(MIN)}$ and VCC clamp voltage.

Protections

Under Voltage Lockout (UVLO)

When the voltage on the VCC pin drops to below $V_{OPR(MIN)}$, the IC stops switching. The IC can restart when the voltage on VCC exceeds the startup voltage ($V_{TH(ST)}$).

Leading-Edge Blanking (LEB)

To prevent false detection of the peak current of the inductor, a blanking time following switch-on is designed. When the internal switch turns on, a short current spike can occur because of the capacitive discharge of the voltage over the drain and source. It is disregarded during the LEB time (ton_min).

Cycle-By-Cycle Over Current Protection (OCP)

The AL16937 has a built-in peak current detector. It triggers when the voltage on CS pin reaches the peak level V_{CS_CLAMP} . The R5 is connected to the CS pin to sense the current of the inductor. The maximum peak current ($I_{PEAK(MAX)}$)) of the inductor can be calculated as below:

$$I_{PEAK(MAX)} = \frac{V_{CS_CLAMP}}{R5}$$
 (1)



The detection circuit is activated after the LEB time. When the detection circuit sense the CS voltage is higher than 1V, the IC will turn off the switching to limit the output current. It automatically provides protection for the maximum LED current during operation. A propagation delay exists between over current detection and actual source-switch off, so the actual peak current is a little higher than the OCP level set by the R5.

Output-Short Protection (OSP)

When LED is shorted, the device cannot detect the toFF time, and the device controls the system operation at 4kHz low frequency.

Thermal Foldback Protection (TFP)

AL16937 has a thermal foldback protection (TFP) function and adopts self-adaptive control method, which can prevent the system breaking down caused by high temperature. The overheating temperature is set at +145°C typical, when the junction temperature of the IC is higher than +145°C typical, the device will linearly decrease the internal reference voltage to decrease the output current. As a result of this feature, the device can control the system's output power at high ambient temperature, to control the quantity of heat of the system. This enhances the safety of the system at high temperature.

Thermal foldback waveform is shown below:

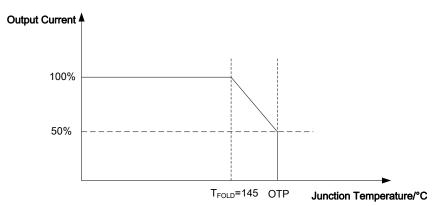


Figure 2 Thermal Foldback Waveform

Over-Temperature Protection (OTP)

The AL16937 has over temperature protection (OTP) function. When the junction temperature reach to +160°C typical, the IC will trigger an over-temperature protection, which causes the device to shut down and latched condition. Once OTP triggered, the system need to be resumed after the system's AC source supply has been reset and power up.

Design Parameters

Setting the Current Sense Resistor R5

In buck structure, when output is larger than input, no energy will be transferred to output, this period is called dead zone, and the dead zone angle is θ .

$$\theta = a \sin \frac{V_O}{\sqrt{2} \cdot V_{IN_RMS}} \quad ... \tag{2}$$

Where,

Vo is the output voltage.

 $V_{\mbox{\scriptsize IN_RMS}}$ is the RMS value of the input voltage.

The AL16937 adopts boundary conduction mode, the output current is calculated as below,

$$I_{O_MEAN} = \frac{1}{\pi} \cdot \int_{\theta}^{\pi-\theta} \frac{1}{2} \cdot i_{pk} \cdot \frac{t_{ON} + t_{OFF}}{t_{ON} + t_{OFF} + t_{DELAY}} dt \approx \frac{1}{2\pi} \cdot \int_{\theta}^{\pi-\theta} i_{pk} \cdot dt$$
 (3)



Where.

ipk is the instantaneous peak current of the inductance

ton is the internal MOSFET on time

toFF is the freewheel diode D1 conduction time

t_{DELAY} is typical 0.4µs

ipk can be expressed as below formula,

$$i_{pk} = \frac{\sqrt{2}V_{IN_RMS}\sin t - V_o}{L_2} \cdot t_{ON} \tag{4}$$

Where

L2 is the inductance value of the L2 inductor

V_{IN RMS} is the input voltage's RMS value

Vo is the system output voltage

So Io_MEAN can be further expressed as below:

$$I_{O_MEAN} = \frac{t_{ON}}{2\pi L_2} \cdot \int_{\theta}^{\pi-\theta} (\sqrt{2}V_{IN_RMS} \sin t - V_o) dt$$
 (5)

The AL16937 is a closed loop constant current control with the relationship between output current and current sense voltage follows this equation

$$V_{REF} = \frac{1}{\pi} \cdot \int_{a}^{\pi-\theta} i_{pk} \cdot R5 \cdot \frac{t_{ON} + t_{OFF}}{t_{ON} + t_{OFF} + t_{DELAY}} dt \approx \frac{1}{\pi} \cdot \int_{a}^{\pi-\theta} i_{pk} \cdot R5 dt$$
 (6)

Where.

V_{REF} is the internal reference, typical 0.4V.

R5 is the current sense resistor

So we can get the output current equation as below,

$$I_{O_MEAN} = \frac{1}{2} \cdot \frac{V_{REF}}{R5} \tag{7}$$

Inductance Selection (L2)

In buck structure, the peak current of the inductor L2 can be calculated from equation (4) when set $t=\frac{\pi}{2}$

$$I_{PEAK} = \frac{\sqrt{2}V_{IN_RMS} - V_o}{L_2} \cdot t_{ON}$$
(8)

From equation (5) (7) (8), the peak current of the inductor L2 can be further expressed as

$$I_{PEAK} = (1 - \frac{V_o}{\sqrt{2}V_{IN_RMS}}) \cdot \frac{\pi \cdot V_{REF}}{R5 \cdot \int\limits_{\theta}^{\pi \cdot \theta} (\sin(\theta) - \frac{V_o}{\sqrt{2}V_{IN_RMS}}) \cdot dt}$$

$$(9)$$

The AL16937 controls the system operating at boundary conduction mode which results in its operating frequency not being constant. To set the minimum switching frequency f_{MIN} at the crest of the minimum AC input.

$$L_2 = \frac{(\sqrt{2}V_{IN_RMS} - V_O) \cdot V_O}{I_{REAV} \cdot \sqrt{2}V_{IN_RMS} \cdot f_{MIN}}$$

$$(10)$$



According to the Faraday's Law, the winding number of the inductance can be calculated by:

$$N_{L2} = \frac{L_2 \cdot I_{PEAK}}{A_1 \cdot B_{...}} \tag{11}$$

Where.

Ae is the core effective area.

B_m is the maximum magnetic flux density.

ton MAX Setting

In order to get a good dimmer compatibility and a good dimming depth, the device sets a t_{ON_MAX} by one external resistor R_T (R6). And the t_{ON_MAX} time has the below equation:

$$t_{ON_MAX} = \frac{3.3 \cdot C_{REF}}{V_{RT_REF}} + 0.33uA \tag{12}$$

Where

V_{RT_REF} is the internal RT pin 0.5V's reference.

CREF is the internal 1.5pF capacitor.

Dimming Control

The AL16937 is a closed loop control device; the dimming function is realized by toN_MAX limited when dimmer is connected in. When the dimmer is at the largest conduction angle, the device still has the adjustability to control the output current constant before COMP voltage is adjusted to the maximum 4V, so for most of the dimmer, the output current is almost the same with the no dimmer condition at the largest conduction angle. If the conduction angle is decreased, the COMP pin voltage will continue to increase quickly till to the maximum level (typical 4V), the device will output toN_MAX to limit system's output current. The toN_MAX is set by RT pin connected with one resistor, so the dimming depth can be adjusted by RT resistor (R6).

Before the AL16937 enters toN_MAX mode, it keeps the output current constant the same as no dimmer condition. When enter toN_MAX mode, we can get the following equation:

$$i_{PEAK_DIM} = \frac{(\sqrt{2}V_{IN_RMS} \cdot S \operatorname{int} - V_o) \cdot t_{ON_MAX}}{L_o}$$
(13)

The peak value of IPEAK_DIM will be clamped to IPEAK_MAX if CS voltage surpasses the VCS_CLAMP voltage.

From the buck output current equation, we can get the output current when dimming:

$$I_{O}(\theta) = \begin{cases} \frac{1}{2} \cdot \frac{V_{REF}}{R5} & \text{if } t_{ON} < t_{ON_MAX} \\ \frac{1}{\pi} \int_{0}^{\alpha} \frac{1}{2} \cdot i_{PEAK_DIM} dt & \text{else} \end{cases}$$

$$(14)$$

Where,

 α is the dimmer conduction angle.



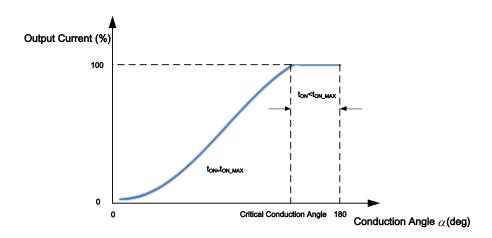


Figure.3 Dimming Curve

Dimmer Compatibility

Passive Bleeder Design

The passive bleeder is designed to supply latching and holding current to eliminate dimmer misfire and flicker.

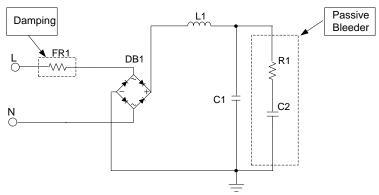


Figure 4 LED Driver Schematic with Passive Bleeder

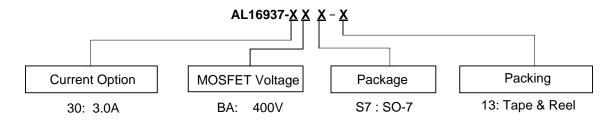
The passive bleeder includes a capacitor (C2, in hundreds of nF) to provide latching current. A resistor (R1) is necessary to dampen the current spike. Because a large C2 will affect the PF, THD and efficiency, the value of the capacitor (C2) should be selected accordingly. Generally, 100nF/400V to 330nF/400V is recommended. R1 is used to limit the latching current, If R1 is too large, the latching current is not enough and the TRIAC dimmer will misfire causing LED flicker. If R1 is too small, it will result in greater power dissipation. Generally speaking, a 200Ω to $2k\Omega$ resistor is selected for R1.

Passive Damping Design

FR-1 is the damper for reducing the spike current caused by quick charging of C2 at firing. In General, FR-1 is selected from 20Ω to 100Ω for low line like $120V_{AC}$ application, and 51Ω to 200Ω for high line like $230V_{AC}$ application.



Ordering Information



Dort Number	Package Code	Doolsono	13" Tape and Reel		
Part Number		Package	Quantity	Part Number Suffix	
AL16937-30BAS7-13	S7	SO-7	4000/Tape & Reel	-13	

Marking Information

(Top View) 7 6 5 Logo Marking ID 16937-30BA for 3.0A/400V 1 2 3 4

<u>YY</u>: Year: 15,16,17 ~ <u>WW</u>: Week: 01~52; 52

represents 52 and 53 week

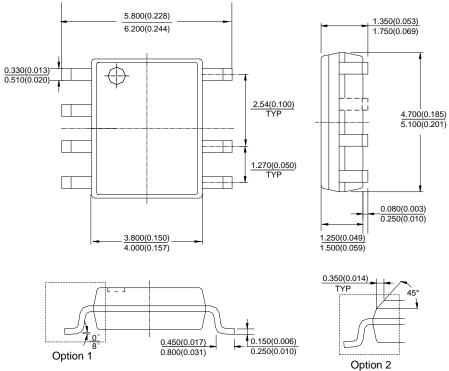
XX: Internal Code

SO-7



Package Outline Dimensions (All dimensions in mm.)

(1) Package Type: SO-7

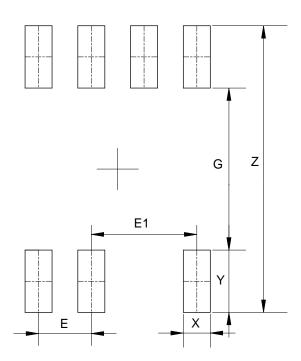


Note: Eject hole, oriented hole and mold mark is optional.



Suggested Pad Layout

(1) Package Type: SO-7



Dimensions	Z	G	X	Υ	E	E1
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050	2.540/0.100



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